

2SA2084

Silicon PNP epitaxial planar type

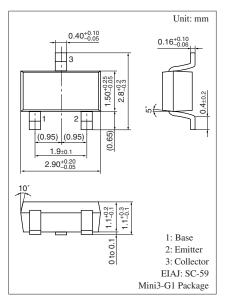
For general amplification

■ Features

- \bullet High collector-emitter voltage (Base open) V_{CEO}
- Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	-300	V	
Collector-emitter voltage (Base open)	V _{CEO}	-300	V	
Emitter-base voltage (Collector open)	V_{EBO}	-5	V	
Collector current	I_C	-70	mA	
Peak collector current	I_{CP}	-100	mA	
Collector power dissipation	P _C	200	mW	
Junction temperature	T_{j}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	



Marking Symbol: 7N

■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

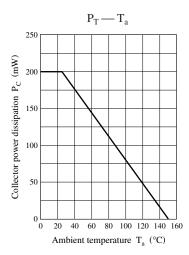
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = -100 \ \mu A, I_B = 0$	-300			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = -1 \mu A_{\rm s}$, $I_{\rm C} = 0$	-5			V
Forward current transfer ratio *	h _{FE}	$V_{CE} = -10 \text{ V}, I_C = -5 \text{ mA}$	30		150	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = -10 \text{ mA}, I_B = -1 \text{ mA}$			- 0.6	V
Collector output capacitance	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		7		pF
(Common base, input open circuited)						
Transition frequency	f_T	$V_{CB} = -10 \text{ V}, I_E = 10 \text{ mA}, f = 200 \text{ MHz}$		50		MHz

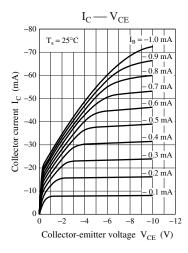
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

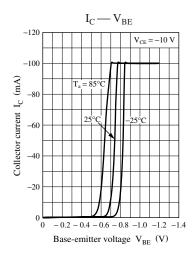
2. *: Rank classification

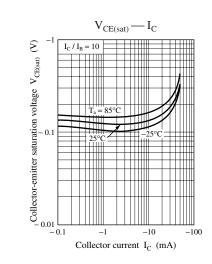
Rank	Р	Q
h_{FE}	30 to 100	60 to 150

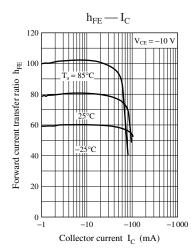
Panasonic

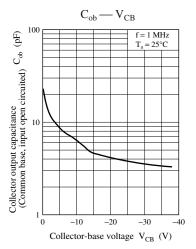












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